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APPLICANTS

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** CONTINUING DATA *****
 This application is a DIV of 10/196,229 07/17/2002 PAT 6,642,564
JAG

** FOREIGN APPLICATIONS *****
 JAPAN 2001-218277 07/18/2001
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Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 6	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged <i>William A. Emery</i> Examiner's Signature	<i>JAG</i> Initials			

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TITLE
 Semiconductor memory and method for fabricating the same

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